



A2/FINE

EXPEDITED PROCEDURE – EXAMINING GROUP 2816

S/N 09/460,742

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Rajendran Nair <i>et al.</i>	Examiner:	Anh Quan Tra
Serial No.:	09/460,742	Group Art Unit:	2816
Filed:	December 14, 1999	Docket No.:	884.229US1
Title:	DEVICE AND METHOD FOR CONTROLLING VOLTAGE VARIATION		
Assignee:	Intel Corporation		

AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.116

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Commissioner for Patents
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This paper is being filed in response to the final office action mailed May 15, 2003.

This response is prepared in the “revised amendment format” announced by the Deputy Commissioner for Patent Examination Policy on June 30, 2003, effective July 30, 2003.

Please amend the above-referenced patent application as follows.